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PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

INVENTOR: C. Ausschnitt et al. )  
SERIAL NO.: 09/977,793 )  
FILING DATE: October 15, 2001 ) DATE: August 13, 2004  
FOR: METHOD FOR DETERMINING SEMICONDUCTOR OVERLAY ON  
GROUND RULE DEVICES

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Dear Sir:

Pursuant to 37 CFR §1.33, please change the correspondence address, (Customer No. 32074), for all purposes for the above identified patent application and resulting patent to:

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Respectfully submitted,

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